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(CORRECTIONS ARE MADE IN BOLD TYPE)

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Application Information

Title Line One:: INTEGRATION SCHEME USING SELF-PLANARIZED
Title Line Two:: DIELECTRIC LAYER FOR SHALLOW TRENCH ISOLATION
Formal Drawings?:: No
Application Type:: Utility
Docket Number:: **AM3299/T29900**
Secrecy Order in Patent Appl.?:: No

Prior Foreign Applications

Foreign Application One::
Filing Date::
Country::
Priority Claimed::

PCT/US00/22561
16 August 2000
PCT
Yes

Foreign Application Two::
Filing Date::
Country::
Priority Claimed::

EP99402068.3
16 August 1999
Europe
Yes